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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/726,641	12/04/2003	Ming-Dou Ker	6720.0117-00	6205

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BERKELEY LAW & TECHNOLOGY GROUP, LLP  
17933 NW Evergreen Parkway, Suite 250  
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EXAMINER
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BENENSON, BORIS

ART UNIT	PAPER NUMBER
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2836

MAIL DATE	DELIVERY MODE
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11/13/2007

PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

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**Office Action Summary**

Application No.

10/726,641

Applicant(s)

KER ET AL.

Examiner

Boris Benenson

Art Unit

2836

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --****Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 02 August 2007.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 4, 28, 31, 44, 48-50, 54, 60, 61 and 65-71 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 4, 28, 31, 44, 48-50, 54, 60, 61 and 65-71 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 11 September 2006 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: \_\_\_\_\_

***Detailed Actions***

1. Amendment received on 8/02/2007 is entered.
  - a. Claims 4, 31, 44, 48-50, 54, 60-61, 65 and 68-69 are amended.
  - b. New Claims 70 and 71 are entered.
  - c. Claims 1-3, 5-27, 29-30, 32-43, 45-47, 51-53, 55-59, and 62-64 are cancelled.
  - d. Claims 4, 28, 31, 44, 48-50, 54, 60-61, and 65-71 are pending in the application.

***Response to the arguments***

2. Applicants argue in reference to amended Claim 4 and 31 that rejection under 35 U.S.C. 102(b) should be withdrawn due changed limitation of deeper contacts receptacles instead of longer contacts into the Claims. Such limitations had been addressed in Office Action of 3/24/2006. New rejection is provided.

3. Applicants argue in reference to Claim 48 that Faraci disclosed a plurality of contact lines where same lines having ends extending to an edge of the board and same lines not reaching the edge of the board, instead of a plurality of

contact receptacles, where some of contact receptacles are deeper than other contact receptacles, as it required by amended language of the Claim. New rejection is provided.

4. The argument relating to rejection of Claims 54 and 60 is basically similar and not convincing.

5. The argument referring to Claim 65 is moot due amendments to the Claim.

***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claims 4, 31, and 70-71 are rejected under 35 U.S.C. 103(a) as being unpatentable over Faraci (4,985,870) in view of Park et al. (5,662,483). Faraci disclosed an Apparatus for Connecting Electronic Modules Containing Integrated Circuits and Backup Batteries. An interface device includes a circuit board (Pos. 12) with a connector (22) formed on the board with a plurality of contact first members - metallic connection traces (24) of a

first length, including a first end connected to the board and a second end (close to the board edge) to connect to an external device (29) and two second contact members (32 and 34) of a second length greater than the first length, wherein a first of two second contact members (32) is connected to a first voltage line (VSS or ground) and a second of two second contact members (34) is connected to a second voltage line (VCC). The second contact members (32 and 34) contact the external device prior to first contact members contacting the external device. "In operation, when the module 10 is inserted into the card edge connector 28, the metallic connection traces 32 and 34 make physical and electrical contact with their respective spring contacts 30 before the other metallic connection traces are contacted. Similarly, when the module 10 is disconnected from the card edge connector 28, the metallic connection traces 32 and 34 remain in electrical contact with their respective spring contacts 30 after the other metallic connection traces have been disconnected from their respective spring contacts" (Col.4, Lines 12-22). Faraci did not disclose a plurality of first contact receptacles being of a first depth and at least two second contact receptacles being of second depth, wherein the second depth is greater than the first depth. Park et al. teach

a Surge Voltage Preventing D-Sub Connector, wherein a receptacle comprises electrically conducting fingers (Fig.6, Pos.76) of the female ground pinholes (56) extend closer to the exterior mating surface (30) than electrically conducting fingers (Fig.7, Pos.77) in the signal pinholes (57). The apparatus of Faraci implement a method of sequential connection: connecting power lines first and, after power is connected, connecting other lines. Faraci achieved such sequence by providing contacts of power lines longer then the rest of the contacts. The same result may be achieved by locating contacts of a receptacle for power close to an edge of the receptacle and the rest of contacts deeper (away from the edge of receptacle) as teaches Park et al. There only two predictable solutions for sequential connection and it would be obvious to try any of any of them. It would have been obvious to one of ordinary skill in the art at the time the invention was made to have modified Faraci with teachings of Park et al. and locate conductive fingers of receptacle on different distance from mating surface, because it will provide proper sequence of power/signal connections. The claims would have been obvious because "a person of ordinary skill has good reason to pursue the known options within his or her technical grasp. If it leads to the anticipated success, it

is likely the product not of innovation but of ordinary skill and common sense." (Supreme Court Decision in KSR International Co. v. Teleflex Inc., 550 U.S.-, 82 USPQ2d 1385).

7. Claims 28, 44, 48-50, 54, 60-61, and 65-69 are rejected under 35 U.S.C. 103(a) as being unpatentable over Samela et al. (6,220,873) in view of Sausen (6,626,682). Samela et al.

disclose a Modified Contact Traces For Interface Converter.

Samela et al. disclose a plurality of first traces read on first pins (Fig, 4b, Pos. 512) formed on a first board (Fig. 4b, Pos 516b), a second board - host socket (Figs. 4b, 6 and 7, pos.710) with a plurality of contact receptacles ((Fig. 4b, Pos 11hs-20hs) to receive the first pins of the first board (Fig. 4b, Pos. 11-20), and a plurality of second pins (Fig.7, Pos 716), formed on second surface of second board, wherein the plurality of receptacles includes one or more contact receptacles that are deeper then the other contact receptacles (Fig. 4b). It is shown on figure 4b, that all of first pins are equal lengths. Samela et al. did not disclose to what type of electronic device the plurality of second pins is adapted to be coupled. Sausen teaches an Integrated Circuit Device Socket for "testing of semiconductor devices during manufacturing" (Col. 1, Lines 14-15). The device comprises a pin housing (Figs 1A-1C, Pos 106).

having a surface with a set of pogo-pins (Pos. 108) to be coupled with contact points (Pos. 112) of semiconductor integrated circuit (Pos. 110). It would have been obvious to one of ordinary skill in the art at the time the invention was made to have modified Samela et al. with teachings of Sausen and adapt the plurality of second pins the way they may be coupled any connection to an integrated circuit to be tested.

### **Contact information**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Boris Benenson whose telephone number is (571) 272-2048. The examiner can normally be reached on Mon -Fri (9-6).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor Michael Sherry can be reached on (571) 272-2084. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications



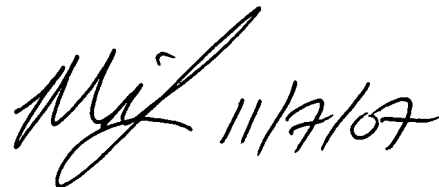
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may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Boris Benenson  
Examiner  
Art Unit 2836

B.B.

A handwritten signature in black ink, appearing to read "M. Sherry", followed by the date "11/7/07".

MICHAEL SHERRY  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2800